

# Pseudo Random Number Generation on FPGA

Tamás Herendi\*\*\*

\* University of Debrecen/Faculty of Informatics, Debrecen, Hungary

**Abstract**—The aim of the present paper is to show the theoretical background of the construction of uniformly distributed (UD) pseudo random number (PRN) sequences

**with good properties and efficient implementation on FPGA.**

## REFERENCES

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